

**LC121S1-A1MT  
12.1" SVGA TFT LCD**

**PRELIMINARY  
SPECIFICATION**

LG.Philips LCD: Rev. 1.3, JAN 30, 2001

**1. GENERAL DESCRIPTION**

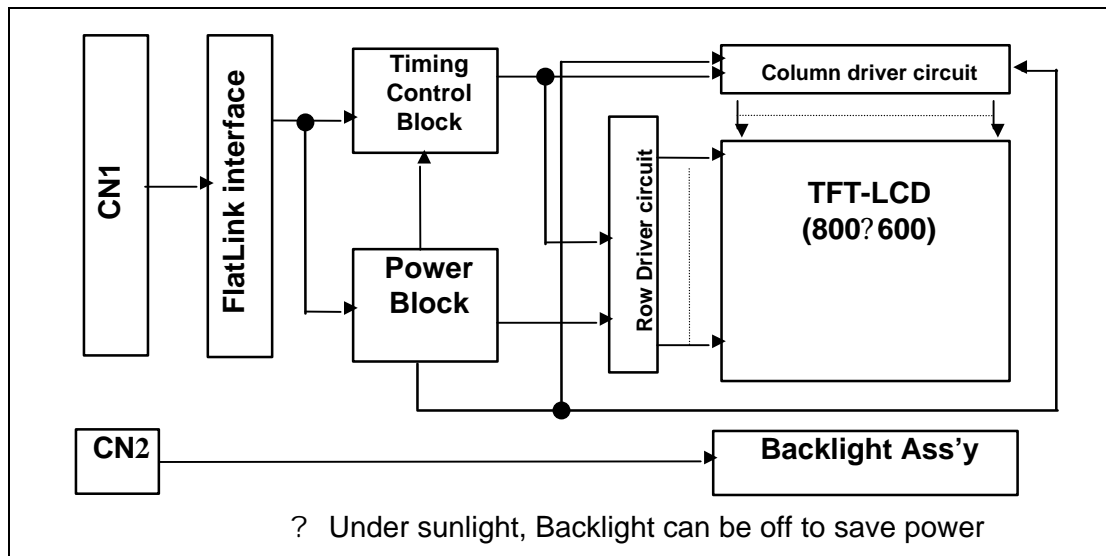
The LC121S1-A1MT is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transflective type<sup>(1)</sup> display operating in the normally white mode. This TFT-LCD has 12.1 inches diagonally measured active display area with SVGA resolution (600 vertical by 800 horizontal pixel array).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LC121S1-A1MT has been designed to apply the interface method that enables low power. Flat Link must be used as a LVDS (Low Voltage Differential Signaling) chip. The LC121S1-A1MT is intended to support applications where thin thickness, low power are critical factors and graphic display are important. In combination with the vertical arrangement of the sub-pixels, the LC121S1-A1MT characteristics provide an excellent flat display for office automation products such as Tough PC.

Note 1: Transflective type means that has a function of transmissive and reflective mode in a body and it is able to use at indoor or outdoor under dark and sunlight at all.

Definition 1: Transflective type is transmissive mode added to reflective mode..



**1.1 General Features**

Active screen size	12.1 inches (30.7 cm) diagonal
Outline dimensions	261 (H) × 199 (V) × 6.5 (D) mm (Typ.)
Pixel pitch	0.1025 mm × RGB × 0.3075 mm
Pixel format	800 horiz. by 600 vert. Pixels (RGB stripe arrangement)
Color depth	6-bit, about 262, 144 colors
Luminance,White	90 cd/m <sup>2</sup> (Typ.)
Power Consumption	Total 4.6 Watt (Typ.) (1 Watt at circuit logic only)
Weight	420 g (Typ.)
Display operating mode	Transflective mode, Normally White
Surface treatments	Hard coating (3 H) No Anti-glare treatment of the fron polarizer

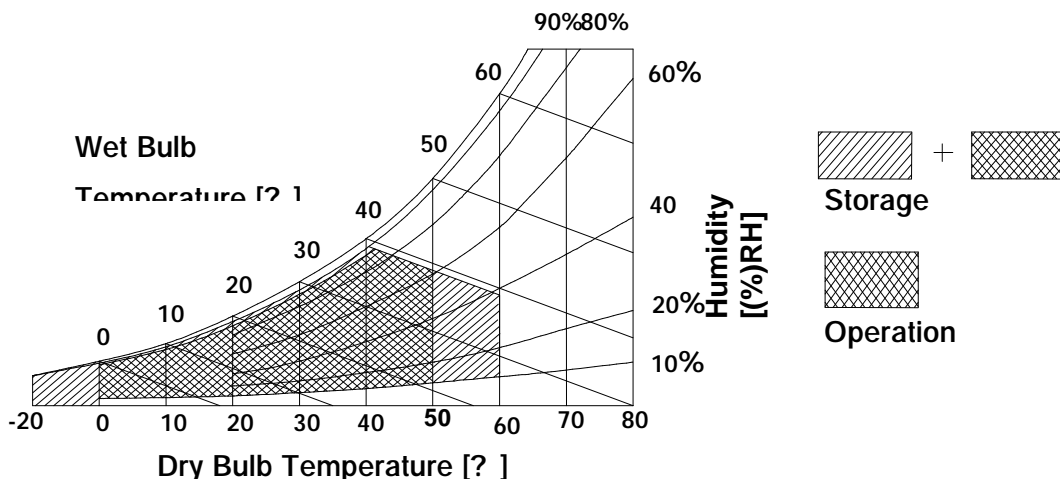
**2. ABSOLUTE MAXIMUM RATINGS**

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1: Absolute maximum ratings

Parameter	Symbol	Values		Units	Notes
		Min.	Max.		
Power Input Voltage	V <sub>CC</sub>	-0.3	4.0	Vdc	at 25 ± 5 °C
Operating Temperature	T <sub>OP</sub>	0	50	°C	1
Storage Temperature	T <sub>ST</sub>	-20	60	°C	1
Operating Ambient Humidity	H <sub>OP</sub>	10	90	%RH	1
Storage Humidity	H <sub>ST</sub>	10	90	%RH	1

Note 1: Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max, and no condensation of water.



### 3. ELECTRICAL SPECIFICATIONS

#### 3.1 Electrical Characteristics

The LC121S1-A1MT requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2: Electrical Characteristics

Parameter	Symbol	Values			Units	Notes
		Min.	Typ.	Max.		
<b>MODULE:</b>						
Power Supply Input Voltage	$V_{CC}$	3.0	3.3	3.6	Vdc	
Power Supply Input Current	$I_{CC}$	-	300	410	mA	1
Differential Impedance	$Z_m$	90	100	110	ohm	2
Power Consumption	$P_c$	-	1.0	1.35	Watts	1
Rush current	$I_{RUSH}$	-	1.6	1.8	A	3
<b>LAMP</b>						
Operating Voltage	$V_{BL}$	540	600	660	$V_{RMS}$	4
Operating Current	$I_{BL}$	3.0	-	6.0	mA	
Established Starting Voltage						5
at 25 °C		-	-	975	$V_{RMS}$	
at 0 °C		-	-	1370	$V_{RMS}$	
Operating Frequency	$f_{BL}$	40	55	(80)	kHz	6
Discharge Stabilization Time	$T_s$	-	-	3	Minutes	7
Power Consumption	$P_{BL}$	-	3.6	4.32	Watts	8
Half-Life Time		10,000	-	-	Hrs	9

Note: The design of the inverter must have specification for the lamp in LCD Assembly. The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in you instrument.

Note 1: The specified current and power consumption are under the  $V_{cc}=3.3V$ , 25°C,  $f_v=60Hz$  condition whereas Mosaic pattern is displayed and  $f_v$  is the frame frequency.

Note 2: This impedance value is needed to proper display and measured from LVDS  $T_x$  to the mating connector.

Note 3: The duration of rush current is about 20ms.

Note 4: The variance of the voltage is  $\pm 10\%$ .

Note 5: The voltage above  $V_s$  should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.

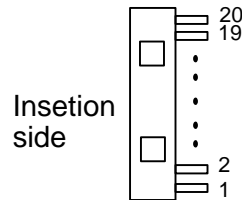
- Note 6: The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- Note 7: Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.  
 $T_s$  is the time required for the brightness of the center of the lamp to be not less than 95%.
- Note 8: The lamp power consumption shown above does not include loss of external inverter.  
The used lamp current is the lamp typical current.
- Note 9: The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at  $25 \pm 2^\circ\text{C}$ .
- Note 10: Do not attach a conducting tape to lamp connecting wire.  
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

### 3.2 Interface Connections

Interface chip must be used FlatLink, part No. THC63LVDM63A(Transmitter), THC63LVDF64A(Receiver) made by Thine Microsystems, Inc.  
This LCD employs two interface connections, a 20 pin connector is used for the module electronics and the other connector is used for the integral backlight system.  
The electronics interface connector is a model DF19K-20P-1H manufactured by HIROSE.  
The pin configuration for the connector is shown in the table below.

Table 3: Module Connector PIN Configuration (LVDS)

Pin	Symbol	Description	Notes
1	Vcc	Power (3.3 V)	1. Interface chips 1.1 LCD: THC63LVDF64A 48TSSOP 1.2 System: THC63LVDM63A 48TSSOP * Pin to Pin compatible with TI LVDS  2. Connector 2.1 LCD: DF19K-20P-1H (HIROSE) 2.2 Mating: Wire Type : DF19G-20S-1C (HIROSE) FPC Type : DF19-20S-1F (HIROSE) 2.3 Connector pin arrangement
2	Vcc	Power (3.3 V)	
3	GND	Ground	
4	GND	Ground	
5	A0M	Differential Signal	
6	A0P	Differential Signal	
7	GND	Ground	
8	A1M	Differential Signal	
9	A1P	Differential Signal	
10	GND	Ground	
11	A2M	Differential Signal	
12	A2P	Differential Signal	
13	GND	Ground	
14	CLKM	Differential Signal	
15	CLKP	Differential Signal	
16	GND	Ground	
17	NC	No Connection	
18	NC	No Connection	
19	GND	Ground	
20	GND	Ground	



The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHSS-1 or equivalent. The pin configuration for the connector is shown in the table below.

Table 4: Backlight Connector PIN Configuration

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Note1: The high voltage side terminal is colored pink. The low voltage side terminal is white.

**Table 5 Required Signal Assignment for FlatLink Transmitter**

Pin #	Pin Name	Require Signals	Pin #	Pin Name	Require Signals
1	D4	R4	48	D3	R3
2	Vcc	Vcc	47	D2	R2
3	D5	R5	46	GND	GND
4	D6	G0	45	D1	R1
5	GND	GND	44	D0	R0
6	D7	G1	43	NC	NC
7	D8	G2	42	LVDS GND	LVDS GND
8	Vcc	Vcc	41	Y0M	A0M
9	D9	G3	40	Y0P	A0P
10	D10	G4	39	Y1M	A1M
11	GND	GND	38	Y1P	A1P
12	D11	G5	37	LVDS Vcc	LVDS Vcc
13	D12	B0	36	LVDS GND	LVDS GND
14	NC	NC	35	Y2M	A2M
15	D13	B1	34	Y2P	A2P
16	D14	B2	33	CLKOUTM	CLKM
17	GND	GND	32	CLKOUTP	CLKP
18	D15	B3	31	LVDS GND	LVDS GND
19	D16	B4	30	PLL GND	PLL GND
20	D17	B5	29	PLL Vcc	PLL Vcc
21	Vcc	Vcc	28	PLL GND	PLL GND
22	D18	HSYNC	27	$\overline{\text{SHDN}}$	$\overline{\text{SHDN}}$
23	D19	VSYNC	26	CLKIN	Dclk
24	GND	GND	25	D20	DE(Data Enable)

Note: Refer to LVDS Transmitter Data Sheet for detail descriptions.

### 3.3 Signal Timing Specifications

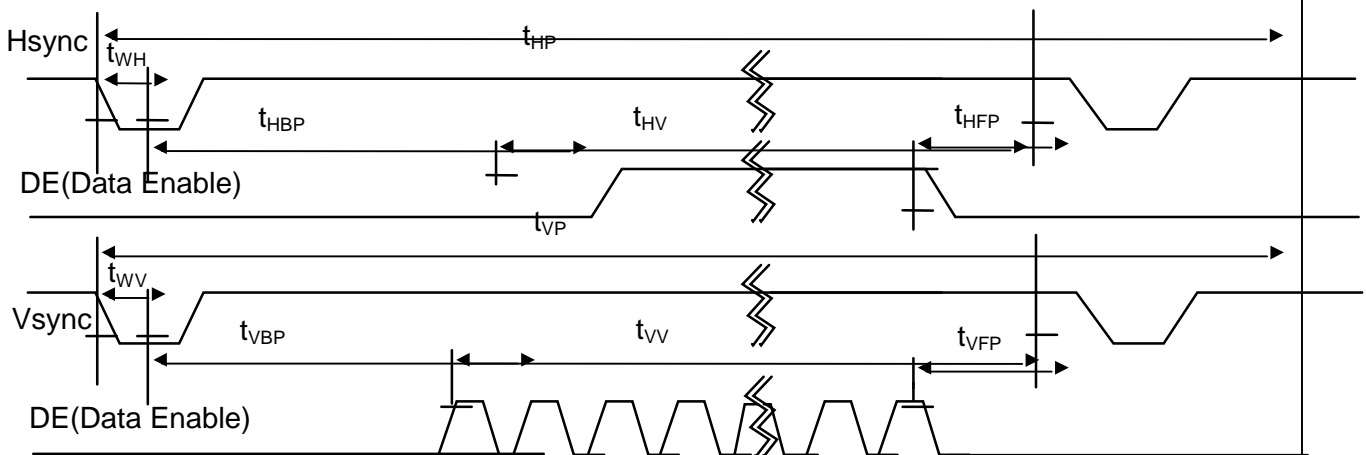
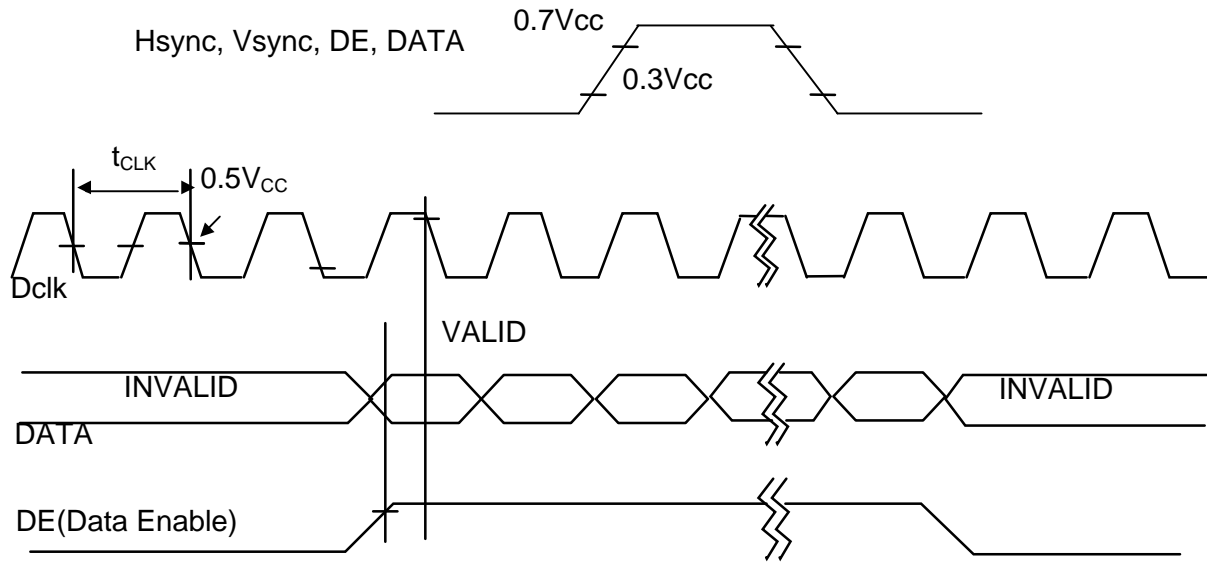
This is the signal timing required at the input of the LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 6: Timing Table

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTES
Dclk	Period	$t_{CLK}$	25	26	27	ns	38.5 MHz
Hsync	Period	$t_{HP}$	990	1024	1100	$t_{CLK}$	
	Width	$t_{WH}$	12	-	120		
Vsync	Period	$t_{VP}$	604	625	730	$t_{HP}$	
	Frequency	$f_V$	60	60	60	Hz	
	Width	$t_{WV}$	1	-	24	$t_{HP}$	
DE ( Data Enable )	Horizontal Valid	$t_{HV}$	800	800	800	$t_{CLK}$	
	Horizontal Back Porch	$t_{HBP}$	30	-	-		
	Horizontal Front Porch	$t_{HFP}$	30	-	-		
	Horizontal Blank	-	19	~	$t_{HP} - t_{HV}$		$t_{WH} + t_{HBP} + t_{HFP}$
	Vertical Valid	$t_{VV}$	600	600	600	$t_{HP}$	
	Vertical Back Porch	$t_{VBP}$	0	-	-		
	Vertical Front Porch	$t_{VFP}$	3	-	-		
	Vertical Blank	-	4	~	$t_{VP} - t_{VV}$		$t_{WV} + t_{VBP} + t_{VFP}$



3.4 Signal Timing Waveforms



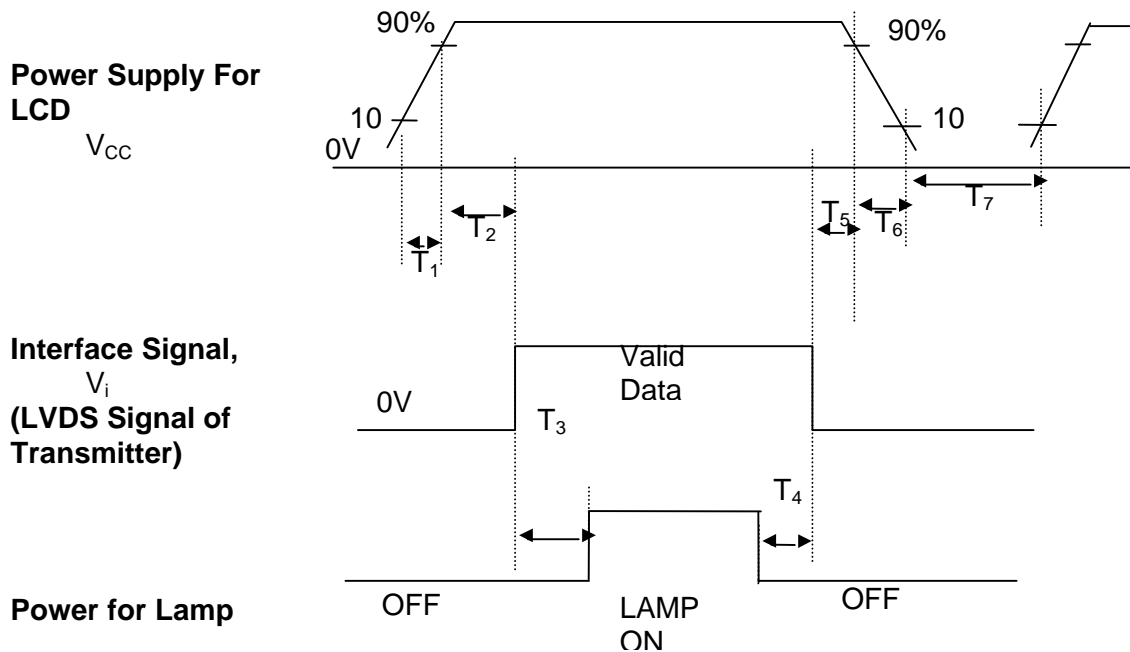
### 3.5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7: Color Data Reference

Color		Input Color Data																	
		Red						Green						Blue					
		MSB			LSB			MSB			LSB			MSB			LSB		
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red(63) Bright	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
Green	Green(00)Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
Green(63)Bright	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
Blue	Blue(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
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	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
Blue(63) Bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

**3.6 Power Sequence**



Parameter	Values			Units
	Min.	Typ.	Max.	
$T_1$	-	-	10	ms
$T_2$	0	-	50	ms
$T_3$	200	-	-	ms
$T_4$	200	-	-	ms
$T_5$	0	-	50	ms
$T_6$	-	-	10	ms
$T_7$	400	-	-	ms

Note 1: Please avoid floating state of interface signal at invalid period.

Note 2: When the interface signal is invalid, be sure to pull down the power supply for LCD  $V_{CC}$  to 0 V.

Note 3: Lamp power must be turn on after power supply for LCD and interface signal are valid.

**4. OPTICAL SPECIFICATIONS**

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50 cm from the LCD surface at a viewing angle of  $\theta$  and  $\phi$  equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

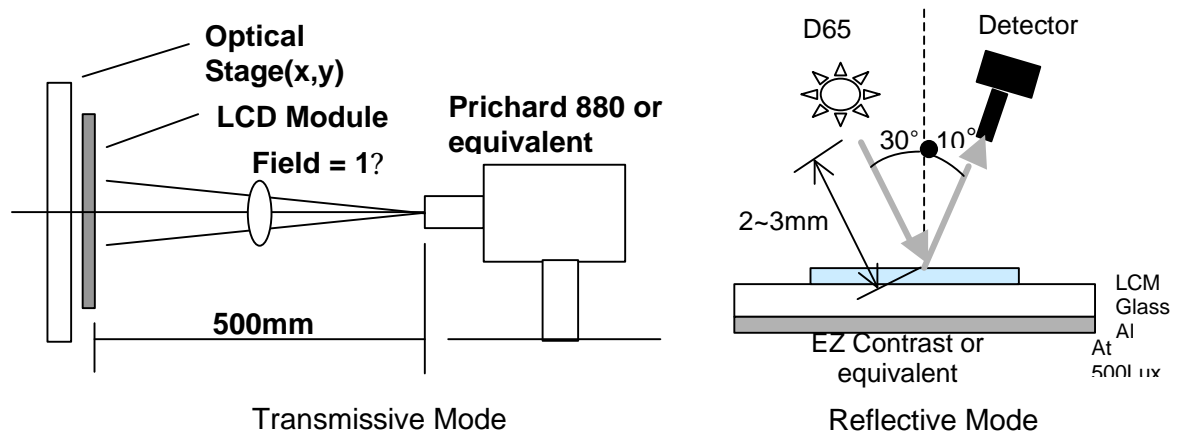


Table 8: Optical Characteristics

( $T_a = 25\text{ °C}$ ,  $V_{cc} = 3.3V$ ,  $f_V = 60Hz$ ,  $D_{clk} = 38.5MHz$ ,  $I_{BL} = 6.0mA$ )

Parameter	Symbol	Values			Units	Notes
		Min.	Typ.	Max.		
Contrast Ratio	CR (T/R)	-	30 / 5	-		1
Reflectance	-	10	12	-	%	By contrast with "BaSO <sub>4</sub> "
Surface Luminance, white	$L_{WH(T+R)}$	75 (=65+10)	90 (=80+10)	-	cd/m <sup>2</sup>	2
Luminance Variation	$d_{WHITE}$	-	1.25	1.45		3
Response Time	$T_r$				ms	4
Rise Time	$T_{rR}$	15	20	-		
Decay Time	$T_{rD}$	25	30	-		
CIE Color Coordinates						
Red	$X_R$	0.339	0.369	0.399		
	$Y_R$	0.253	0.283	0.313		
Green	$X_G$	0.282	0.312	0.342		
	$Y_G$	0.352	0.382	0.412		
Blue	$X_B$	0.192	0.222	0.252		
	$Y_B$	0.232	0.262	0.292		
White	$X_W$	0.275	0.305	0.335		
	$Y_W$	0.285	0.315	0.345		
Viewing Angle					degree	5
x axis, right ( $F = 0^\circ$ )	$\theta_r$	20	-	-		(T at CR=5)
x axis, left ( $F = 180^\circ$ )	$\theta_l$	20	-	-		Reflective is not measured
y axis, up ( $F = 90^\circ$ )	$\theta_u$	20	-	-		
y axis, down ( $F = 270^\circ$ )	$\theta_d$	40	-	-		
Gray Scale		-	-	-		6

T : Transmissive Mode, R: Reflective Mode

Note 1: Contrast Ratio (CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

Note 2: Surface luminance is the center point across the LCD surface 500 mm from the surface with all pixels displaying white. For more information see FIG. 2.

When  $I_{BL}=6.0$  mA,  $L_{WH}= 80$  cd/m<sup>2</sup> (Typ.) under Transmissive Mode

When  $I_{BL}=0.0$  mA,  $L_{WH}=10$  cd/m<sup>2</sup>(Typ.) under reflective Mode at 500 Lux, offset angle = 20 degree

Note 3: The variation in surface luminance, dWHITE is determined by measuring  $L_{ON}$  at each test position 1 through 5, and then dividing the maximum  $L_{ON}$  of 5 points luminance by minimum  $L_{ON}$  of 5 points luminance. For more information see FIG. 2

$$? \text{ WHITE} = \text{Maximum} (L_{ON1}, L_{ON2}, \dots, L_{ON5}) \div \text{Minimum} (L_{ON1}, L_{ON2}, \dots, L_{ON5})$$

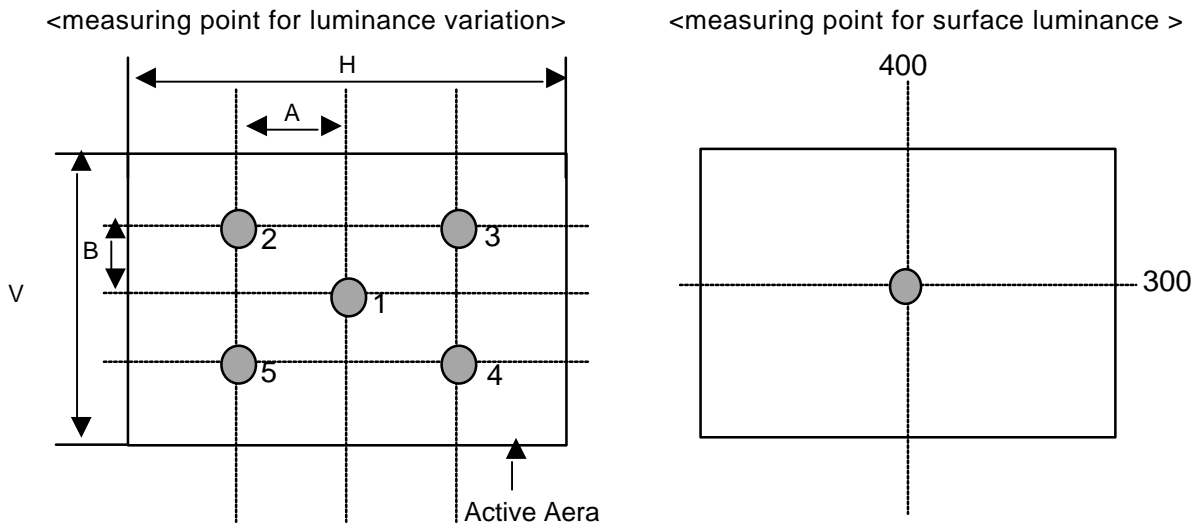
Note 4: Response time is the time required for the display to transition from white to black (Rise Time,  $T_{rR}$ ) and from black to white (Decay Time,  $T_{rD}$ ). For additional information see FIG. 3.

Note 5: Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG. 4

Note 6: Gray scale specification.

Gray Level	Luminance(%) (Typ.)
L 0	1.5
L 7	2.4
L 15	5.0
L 23	9.8
L 31	20.0
L 39	34.0
L 47	54.0
L 55	77.0
L 63	100

FIG. 2 Luminance



- A: H/4 mm
- B: V/4 mm
- H: 246.0 mm
- V: 184.5 mm
- @ H,V: Active Area

FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

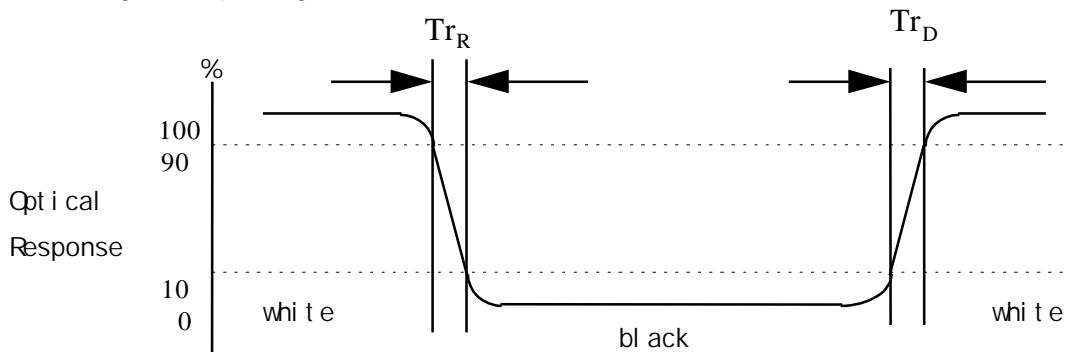
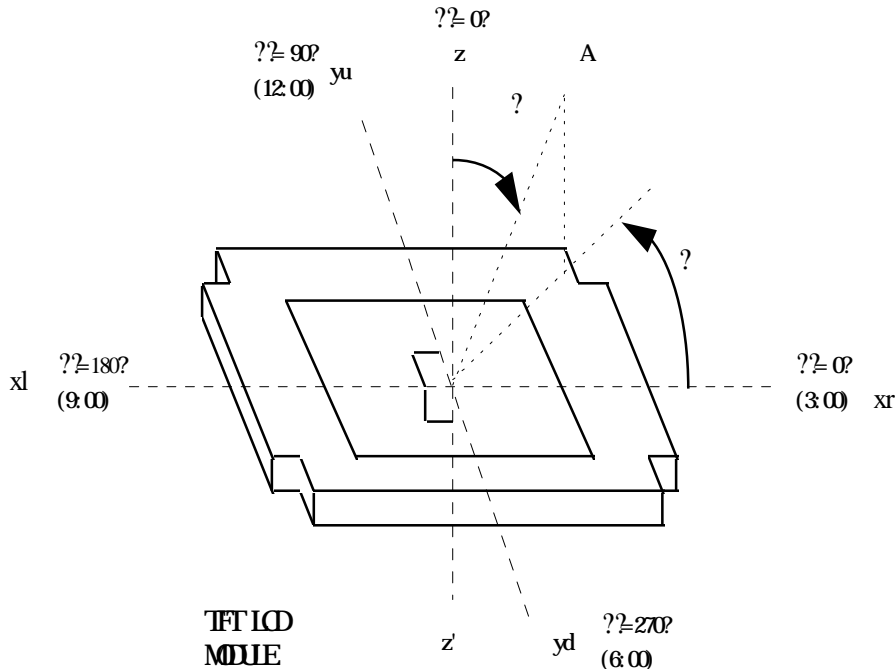


FIG. 4 Viewing angle

<Definition of viewing angle range>



5. MECHANICAL CHARACTERISTICS

The contents provide general mechanical characteristics for the model LC121S1 LCD. In addition, the figures in the next page are detailed mechanical drawings of the LCD.

Outside dimensions:

Horizontal	261 ± 0.5 mm
Vertical	199 ± 0.5 mm
Depth	6.5 ± 0.3 mm

Bezel area:

Horizontal	249.2 ± 0.5 mm
Vertical	187.6 ± 0.5 mm

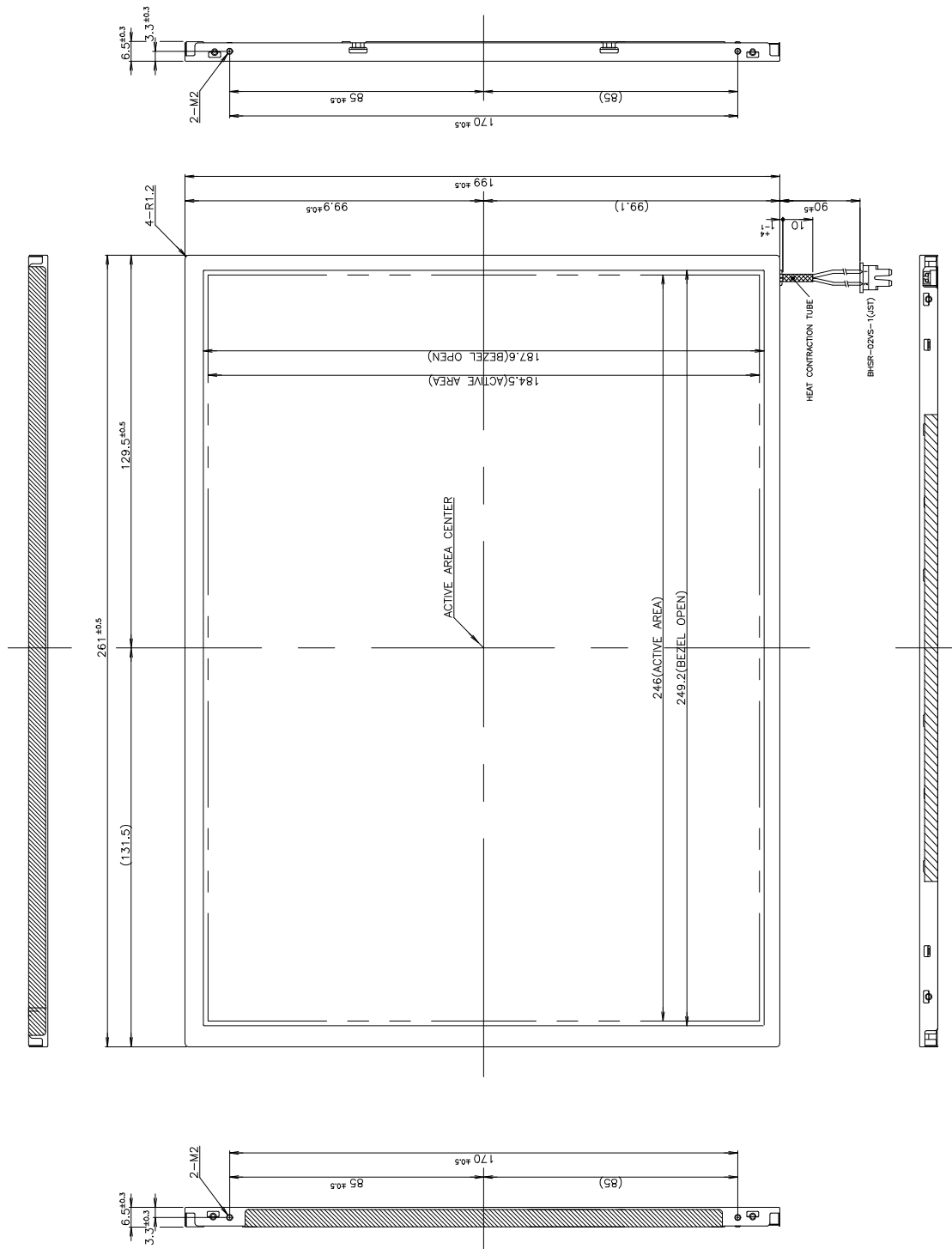
Active Display area:

Horizontal	246.0 mm
Vertical	184.5 mm

Weight (approximate): 420 g (Typ.), 430 g (Max.)

Surface Treatment: Hard coating (3 H)  
No anti-glare treatment of the front polarizer

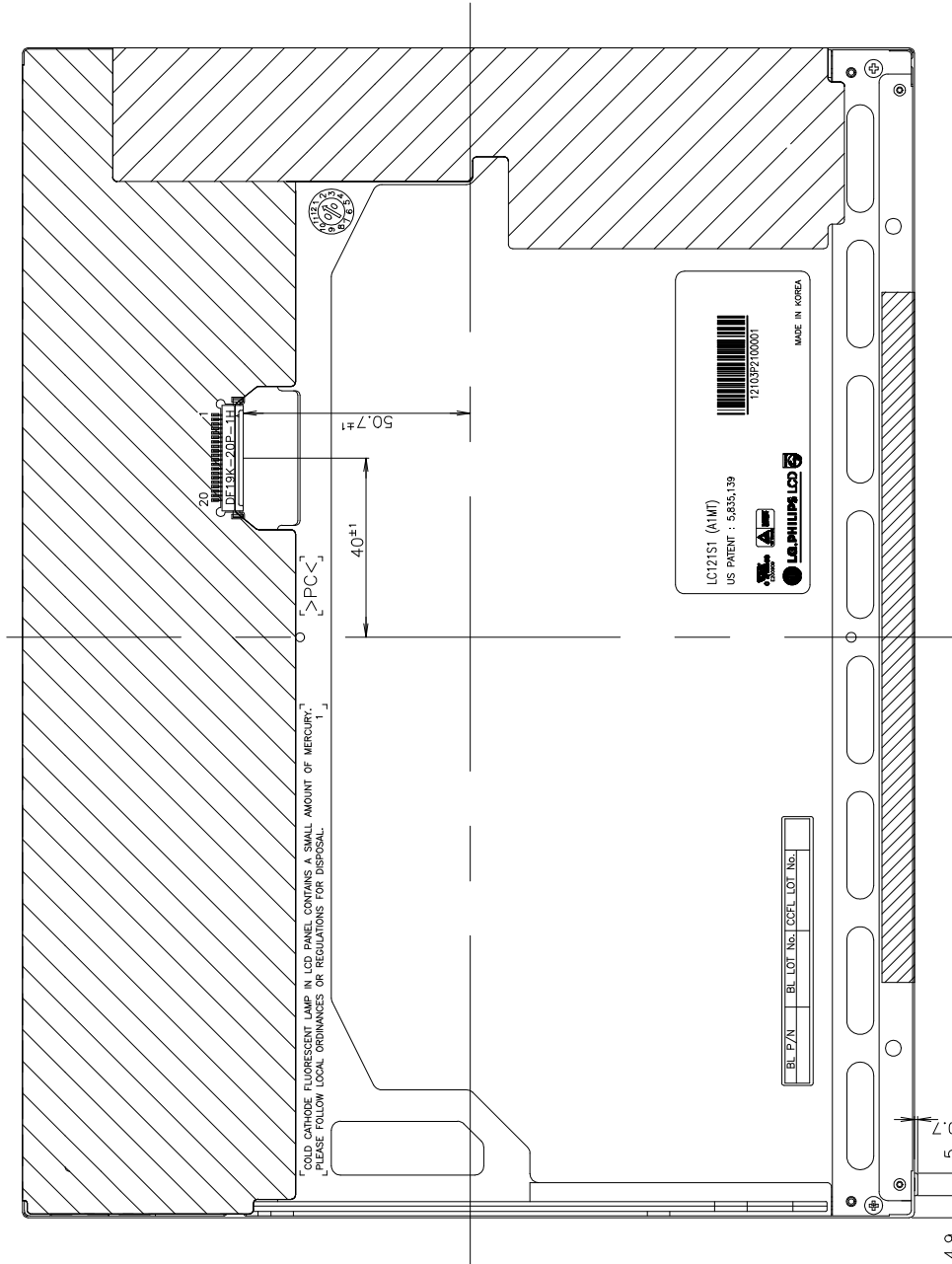
FRONT VIEW



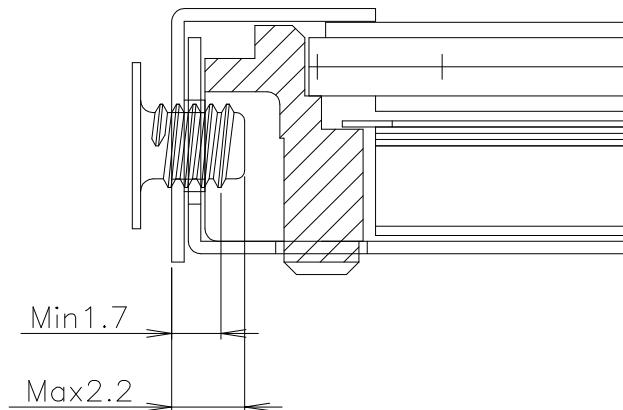
- Note 1: Unspecified dimensional tolerance are  $\pm 0.5\text{mm}$
- Note 2: Please don't attach the electrically-conductive tape around the wire of backlight assy.



**REAR VIEW**



DETAIL DESCRIPTION OF SIDE MOUNTING SCREW



\*SCREW TORQUE : max 2.0kgf.cm

Note 1: Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particels from screw surface

**6. RELIABILITY**

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta = 60 °C 240 h
2	Low temperature storage test	Ta = - 20 °C 240 h
3	High temperature operation test	Ta = 50 °C 50% RH 240 h
4	Low temperature operation test	Ta = 0 °C 240 h
5	Vibration test (non-operating)	Sine wave, 10~500~10 Hz, 1.5G, 0.37 oct/min, 3 axis, 1 hour/axis
6	Shock test (non-operating)	Half sine wave, 220 G, 2 ms, one shock of each six faces (i.e. run 220 G 2 ms for all six faces.)
7	Altitude operating storage/shipment	0 - 10,000 feet (3048 m) 0 - 40,000 feet (12192 m)

Result Evaluation Criteria

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

**7. INTERNATIONAL STANDARDS**

**7.1 Safety**

- a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.  
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.  
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- c) EN 60950 : 1992 + A1 : 1993 + A2 : 1993 + A3 : 1995 + A4 : 1997 + A11 : 1997  
IEC 950 : 1991 + A1 : 1992 + A2 : 1993 + A3 : 1995 + A4 : 1996  
European Committee for Electrotechnical Standardization (CENELEC)  
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

**7.2 EMC**

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9kHz to 40GHz." American National Standards Institute(ANSI),1992.
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interference Characteristics of Information Technology Equipment". International Special Committee on Radio Interference
- c) EN 55022 "Limits and Methods of Measurement of Radio Interference Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization (CENELEC),1988

**8. PACKING**

**8.1 Designation of Lot Mark**

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
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A, B ,C: SIZE  
 D: YEAR  
 E: MONTH  
 F,G: PANEL CODE  
 H: ASSEMBLY CODE  
 I,J,K,L,M: SERIAL NO.

Note1: YEAR

YEAR	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

Note 2: MONTH

MONTH	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

b) Location of Lot Mark

Serial NO. Is printed on the label. The label is attached to the backside of the LCD module.

This is subject to change without prior notice.

**8.2 Packing Form**

a) Package quantity in one box: 10 pcs

b) Box Size: 323 mm × 272 mm × 276 mm

## 9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

### 9.1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to the module.  
And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach a transparent protective plate to the surface in order to protect the polarizer.  
Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.  
Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

### 9.2 OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage:  $V = ? 200 \text{ mV}$  (Over and under shoot voltage).
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)  
And in lower temperature, response time (required time that brightness is stable after turned on ) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

### 9.3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9.4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

### 9.5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5 °C and 35 °C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

### 9.6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer.  
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.  
Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.